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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ **1. TDRC-a symbolic simulation based design for testabil**
Varma, P.;
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10-14 Sept. 1990 Page(s):1055 - 1064
Digital Object Identifier 10.1109/TEST.1990.114130
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- ☐ **2. The input/output specifications analyzer for IC design:**
Lee, E.S.; Chang, H.T.; Wu, K.W.; Kovesdi, R.;
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15-18 May 1989 Page(s):19.1/1 - 19.1/4
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Lazareck, L.; Peters, J.F.;
Electrical and Computer Engineering, 2001. Canadian Con
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13-15 Aug. 1997 Page(s):104 - 108
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- ☐ **5. Developing a distributed architecture for design rule c**
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15-18 Sept. 1999 Page(s):251 - 256
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- ☐ **10. Module compilation for analog and mixed analog digit**
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- ☐ **12. An environment for the development of knowledge ba**

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